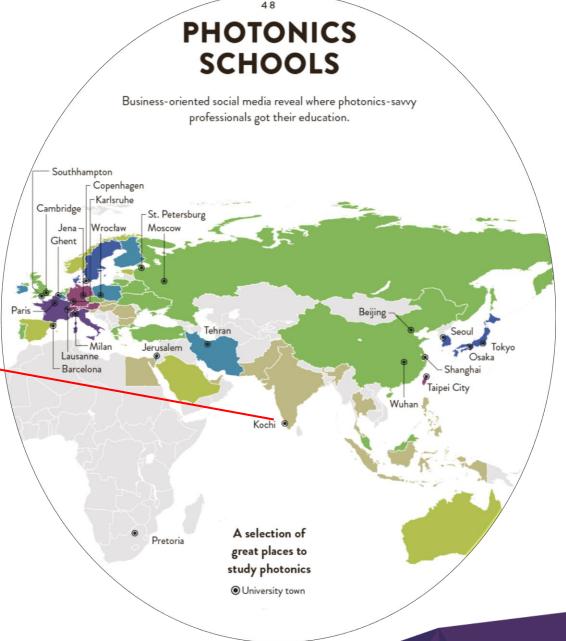


Education

- Master of Science(MSc) Physics
- Master of Technology(MTech)- Photonics





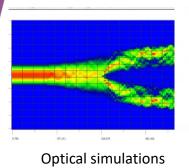
Scientist



Society for Applied Microwave Electronics Engineering and Research



R&D lab, IIT Mumbai,India















Photonic integrated circuits, RF, and Packaging

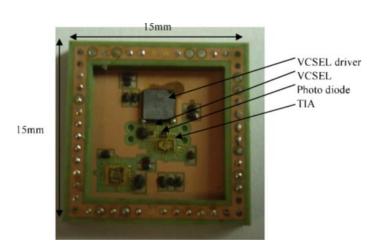


Senior Research Engineer





IME, Singapore





Optoelectronic systems, Medical devices, Advanced packaging



Principal Engineer





Singapore, US, Dresden



12" Silicon CMOS foundry











Doctoral Researcher



After 14 years of Industry and Industrial research experience, I joined

Leeds (SCAPE) as a full time PhD student in Dec 2013!!!





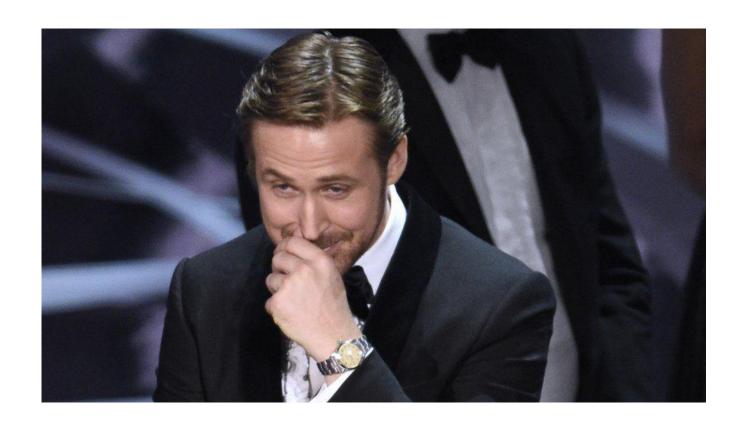


On scholarship



PhD Student

But, I was determined to complete my PhD in 2 years





Doctoral Research



First 6 months.....
I worked very long hours......

Planned the DoEs, prepared 100's of samples.....





Submitted a MSCA experienced researcher (incoming fellow) application







PhD life

After 6 months..... Five- year-old son joined me in the UK.....

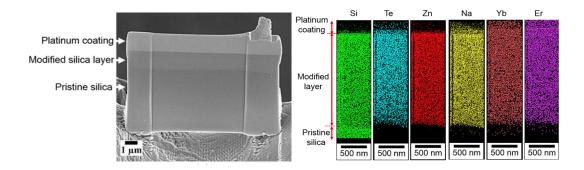
Single parent....

My time was limited **9 am-5 pm** in the university, but the most **productive** period

PhD



Investigated novel optical amplifier materials



Patent: WO2017025759A1





Start-up

In 2 years....on 18 Dec 2015 submitted PhD thesis





Recognitions



Received £ 5.2 million funding from EPSRC, for 5 years

Electronics Weekly.com

Leeds pilots Ultrafast Laser Plasma Implantation for photonic chips

A £5.2 million project led by the University of Leeds is aiming to transform data communications with advances in photonic chips.



RSC Emerging Technology (Materials)
Award-2015

Top 3 PGR of the Year 2015



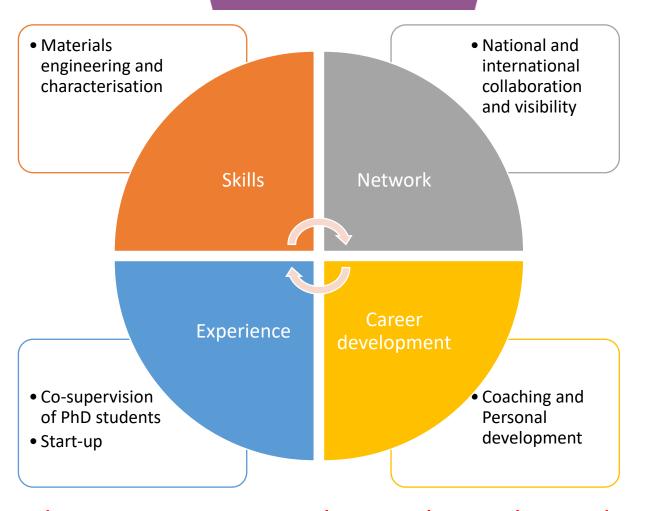


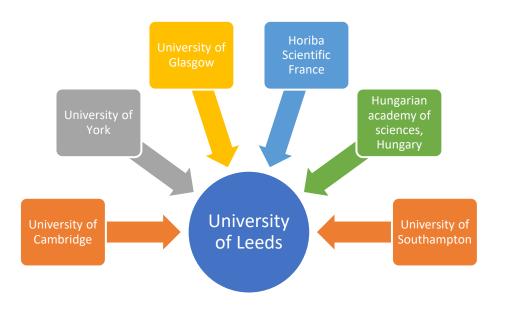
Marie-Curie Individual Fellowship 2016-2017 Health and Environmental sensors

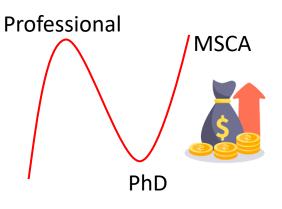
- Co-supervision of 3 PhD students
- Consultant for Seamatics project



PhD & MSCA







Study at LEEDS--> a complete package, changed my outlook



MSCA 2016-2017

- A unique researcher with international experience
 - Industry, RTO and Academia
- Started looking for job towards the last quarter of the tenure
 - Use social network, job sites and personal contacts



2019



Head of Advanced packaging Microelectronics integration



Career Path

Society for Applied
Microwave Electronics
Engineering and Research
Scientist
R&D lab, Govt of India
Photonic integrated
circuits, RF, and Packaging

2010
FILES

Singapore, US, Dresden
Principal Engineer
300mm Silicon CMOS foundry

2014

UK

UNIVERSITY OF LEEDS

Doctoral researcher

Materials engineering

Horizon 2020

Marie-Curie Fellow

Mid-IR Optical materials

CATAPULT
Compound Semiconductor Applications
Head of Advanced packaging
Electronics integration



Singapore

Senior Research Engineer

Optoelectronic systems, Medical devices Advanced packaging







2018

Technology manager Healthcare Photonics

Tips

- Set clear goals and work towards it
- Make yourself essential- Excel in your area
- Take up the opportunities that come along- Be confident
- Expand your network





Thank you